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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/032,761	12/27/2001	Keizo Takechi	16869N-039000US	5771
20350	7590	12/03/2003	EXAMINER	
TOWNSEND AND TOWNSEND AND CREW, LLP			NGUYEN, JIMMY	
TWO EMBARCADERO CENTER				
EIGHTH FLOOR			ART UNIT	
SAN FRANCISCO, CA 94111-3834			2829	
			PAPER NUMBER	

DATE MAILED: 12/03/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/032,761

Applicant(s)

TAKECHI ET AL.

Examiner

Jimmy Nguyen

Art Unit

2829

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(e). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 11 August 2003.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 6-9, 12 and 15-19 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 6-9, 12 and 15-19 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____
- 4) ☐ Interview Summary (PTO-413) Paper No(s) _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other:

DETAILED ACTION

Response to Argument

The examiner acknowledges that claims 6 –9, 12, 15 – 19 are pending. Claims 1-5, 10, 11, 13, and 14 have been canceled. New claims 16 –19 have been added.

The applicant argues that Takeuchi is directed to providing an IC having a driver circuit and a load current output circuit. In claim 6, two buffer circuits and a diode bridge are coupled to provide a direct current to a dut. Takeuchi, on the other hand, is directed to control rise and fall time of a driver circuit. The examiner is disagree, event though the abstract of Takeuchi is direct to control rise and fall time of a driver circuit, however, the structure circuit of Takeuchi is also direct to the two buffer circuits and a diode bridge are coupled to provide the current for dut. Further, there is no indication in Takeuchi that output of a diode bridge 6i is providing the alternating current to the DUT, in order to achieve the control over the rise and fall time of a driver circuit, Takeuchi is alternate the frequency of applied signal

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 6 – 9, 12, 15, 17 –19, are rejected under 35 U.S.C. 102(b) as being anticipated by Takeuchi (US 4523312).

As to claims 6, 7, 17,- 19, Takeuchi discloses (fig 5)

A driver circuit integrated with a load current output circuit (1) comprising: a first buffer circuit (39i) having a first output terminal; a second buffer circuit (41i) having a second output terminal; and a diode bridge terminal (6i) having a pair of a third and fourth output terminals each connected to first (3i) and second output terminals (41i) in the form of direct current , wherein fourth output terminal (connect to 5i) supplies a current as a driver when outputting a test waveform to a DUT (10), and DUT (10) supplies a load current to fourth output terminal therefrom by turning the second buffer (41i) circuit OFF when judging a status of a response waveform by receiving waveform from DUT (10).

As to claims 8 and 9, 12, 15, Ic tester provided with driver circuit integrated with load current output circuit (fig 5). The bridge circuit is simultaneously turned ON, and another diode opposed thereto is simultaneously turned OFF.

Conclusion

Accordingly, Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action.

1. **THIS ACTION IS MADE FINAL.** See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE**

MONTHS from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jimmy Nguyen at (703) 306-5858. Any inquiry of a general nature of relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 305-4900.

JN.
Nov 18, 2003

David A. Farnick
David A. Farnick
Primary Examiner
11/15/03